



## QL24N50

500V N-Channel MOSFET

## General Description

The 24N50 is fabricated using an advanced high voltage MOSFET process that is designed to provide excellent RDS(ON) . These devices are well suited for high efficient switched mode power supplies and active power factor correction.

## Features

- Low on-resistance
- Fast Switching
- RoHS Compliant

## Product Summary

BVDSS	RDS(on)	ID
500V	0.2Ω	24A

## Applications

- DC-AC converters
- SMPS Power
- UPS (Uninterruptible Power Supply)

## TO247 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	500	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	24	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	15	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	96	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	810	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	290	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	40	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	0.43	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit	
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	500	---	---	V	
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=12\text{A}$	---	---	0.2	$\Omega$	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	2	---	4	V	
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=500\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$	
		$V_{\text{DS}}=400\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_C=125^\circ\text{C}$	---	---	10		
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA	
$g_{\text{fs}}$	Forward Transconductance <sup>3</sup>	$V_{\text{DS}}=15\text{V}$ , $I_D=13\text{A}$	---	31	---	S	
$Q_g$	Total Gate Charge	$I_D = 24\text{A}$	---	90	---	nC	
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=400\text{V}$	---	22	---		
$Q_{\text{gd}}$	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	(Note 3, 4)	45	---		
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=250\text{V}$		100	---	ns	
$T_r$	Rise Time	$I_D = 24\text{A}$		250	---		
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=25\Omega$		200	---		
$T_f$	Fall Time	(Note 3, 4)		150	---		
$C_{\text{iss}}$	Input Capacitance			6300	---	pF	
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$		600	---		
$C_{\text{rss}}$	Reverse Transfer Capacitance			65	---		

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	24	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	72	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=24\text{ A}$ , $T_J=25^\circ\text{C}$	---	---	1.4	V

Note :

1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. $L = 5\text{mH}$ ,  $I_d = 18\text{A}$ ,  $V_{\text{DD}} = 50\text{V}$ , Starting  $T_J = 25^\circ\text{C}$ 3.Pulse Test: Pulse width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 2\%$ 

4.Essentially Independent of Operating Temperature

This product has been designed and qualified for the consumer market.

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Cmos reserves the right to improve product design ,functions and reliability without notice.