

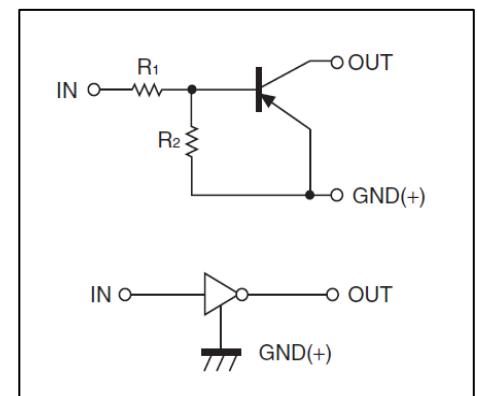


DTA143ECA

Digital Transistors (Built-in Resistors)

DIGITAL TRANSISTOR (PNP)

• Equivalent Circuit



MARKING: 13

FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

PIN CONNECTIONS and MARKING

DTA143EE	SOT-523	DTA143EUA	SOT-323
	1. IN 2. GND 3. OUT		1. IN 2. GND 3. OUT
DTA143EKA	SOT-23-3L	DTA143ECA	SOT-23
	1. IN 2. GND 3. OUT		1. IN 2. GND 3. OUT

**MAXIMUM RATINGS(Ta=25°C unless otherwise noted)**

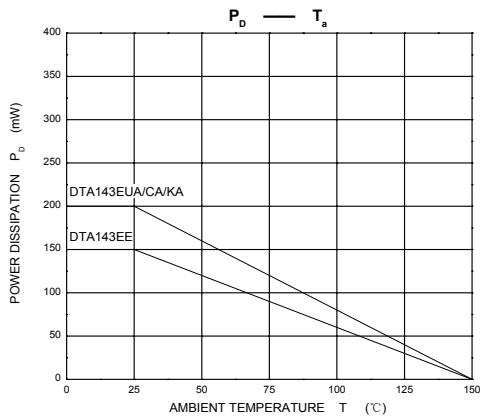
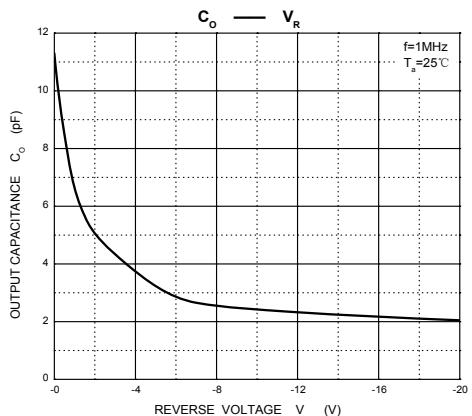
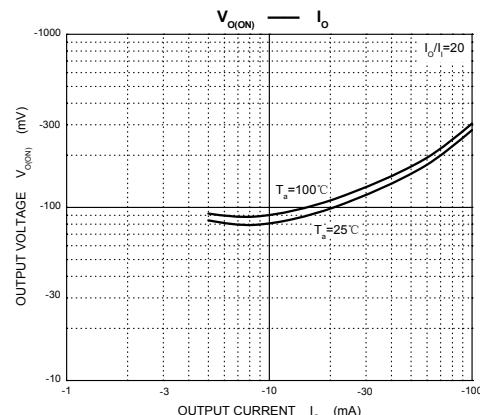
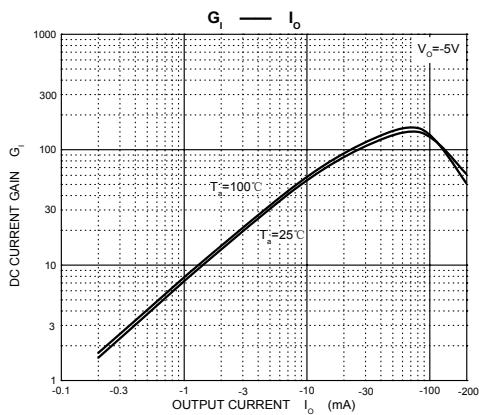
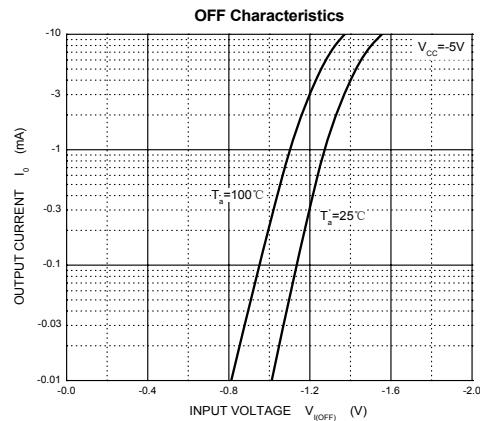
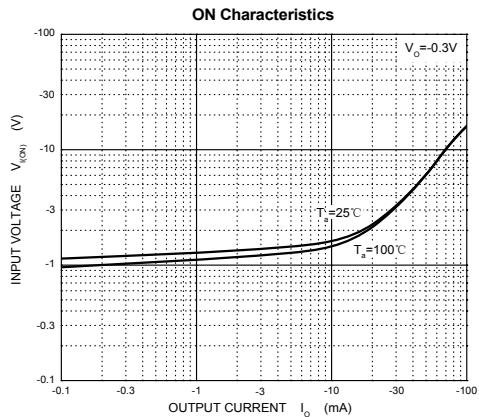
Symbol	Parameter	Limits(DTA143E□)				Unit	
		E	UA	KA	CA		
V _{cc}	Supply Voltage	-50					
V _{IN}	Input Voltage	-30~+10					
I _o	Output Current	-100					
P _D	Power Dissipation	150	200	200	200	mW	
T _j	Junction Temperature	150					
T _{stg}	Storage Temperature	-55~+150					

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =-5V,I _o =-100μA	-0.5			V
	V _{I(on)}	V _O =-0.3V,I _o =-20 mA			-3	V
Output voltage	V _{O(on)}	I _O /I _I =-10mA/-0.5mA			-0.3	V
Input current	I _I	V _I =-5V			-1.8	mA
Output current	I _{O(off)}	V _{CC} =-50V,V _I =0			-0.5	μA
DC current gain	G _I	V _O =-5V,I _o =-10mA	30			
Input resistance	R _I		3.29	4.7	6.11	kΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O =-10V,I _o =-5mA,f=100MHz		250		MHz



Typical Characteristics

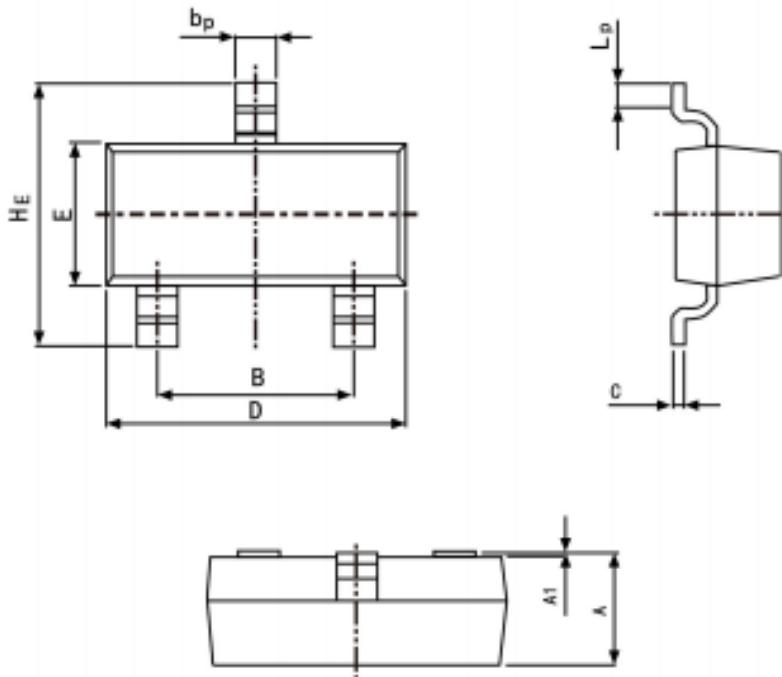




PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



Symbol	Dimension in Millimeters	
	Min	Max
A	0.95	1.40
B	1.78	2.04
bp	0.35	0.50
C	0.08	0.19
D	2.70	3.10
E	1.20	1.65
HE	2.20	3.00
A1	0.100	0.013
Lp	0.20	0.50